

Docket JP920010326US1

Appl. No.: 10/736,343
Filed: December 15, 2003**In the United States Patent and Trademark Office**

In re the application of:)	
Rajendra K. Bera)	
)	
Filed:12/15/2003)	Group Art Unit: 2183
)	
For: Run-Time Parallelization)	Examiner: Ryan Paul Fiegle
of Loops in Computer)	
Programs)	
)	
Appl. No.: 10/736,343)	
)	
Applicant's Docket:)	
JP920010326US1)	

REPLY A

The following amendment is responsive to Examiner's Office communication of May 5, 2006.